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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Application of

Applicant: Haowen Bu, et al.

Docket Number: TI-36637

Serial No.: **10/810,905**

Art Unit: 2823

Filed: 03/26/04

Examiner: Jarrett J. Stark

For: Improved CMOS Transistors and Methods of Forming  
Same

**CERTIFICATION OF FACSIMILE TRANSMISSION**

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<input checked="" type="checkbox"/> <b>FACSIMILE COVER SHEET</b>	<input type="checkbox"/> <b>AMENDMENT</b> (# pages)
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<input type="checkbox"/> <b>CONTINUATION APP'N</b>	<input checked="" type="checkbox"/> <b>ELECTION</b>
<input type="checkbox"/> <b>DIVISIONAL APP'N</b>	
<b>NAME OF INVENTOR(S):</b> Haowen Bu, et al.	
<b>RECEIPT DATE &amp; SERIAL NO.:</b> Serial No.: <b>10/810,905</b>	
<b>TITLE OF INVENTION:</b> Improved CMOS Transistors and Methods of Forming Same Filing Date: 03/26/04	
<b>TI FILE NO.:</b> <b>TI-36637</b>	<b>DEPOSIT ACCT. NO.:</b> <b>20-0668</b>
<b>FAXED:</b> 3-8-06 <b>DUE:</b> 4-1-06	
<b>ATTY/SECY:</b> RAK/kjv	

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Texas Instruments Incorporated  
PO Box 655474, M/S 3999  
Dallas, TX 75265-5474

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Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

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Karen Vertz  
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3-8-06  
Date

**ELECTION**

This election is offered in response to the Examiner's restriction requirement mailed March 1, 2006.

Applicants hereby provisionally elect to pursue Group I with Claims 1-10, without traverse.

Respectfully submitted,

Texas Instruments Incorporated  
P.O. Box 655474, M/S 3999  
Dallas, TX 75265  
(972) 917-4167



Rose Alyssa Keagy  
Attorney for Applicants  
Registration No. 35,095